



FIG. 1(a)

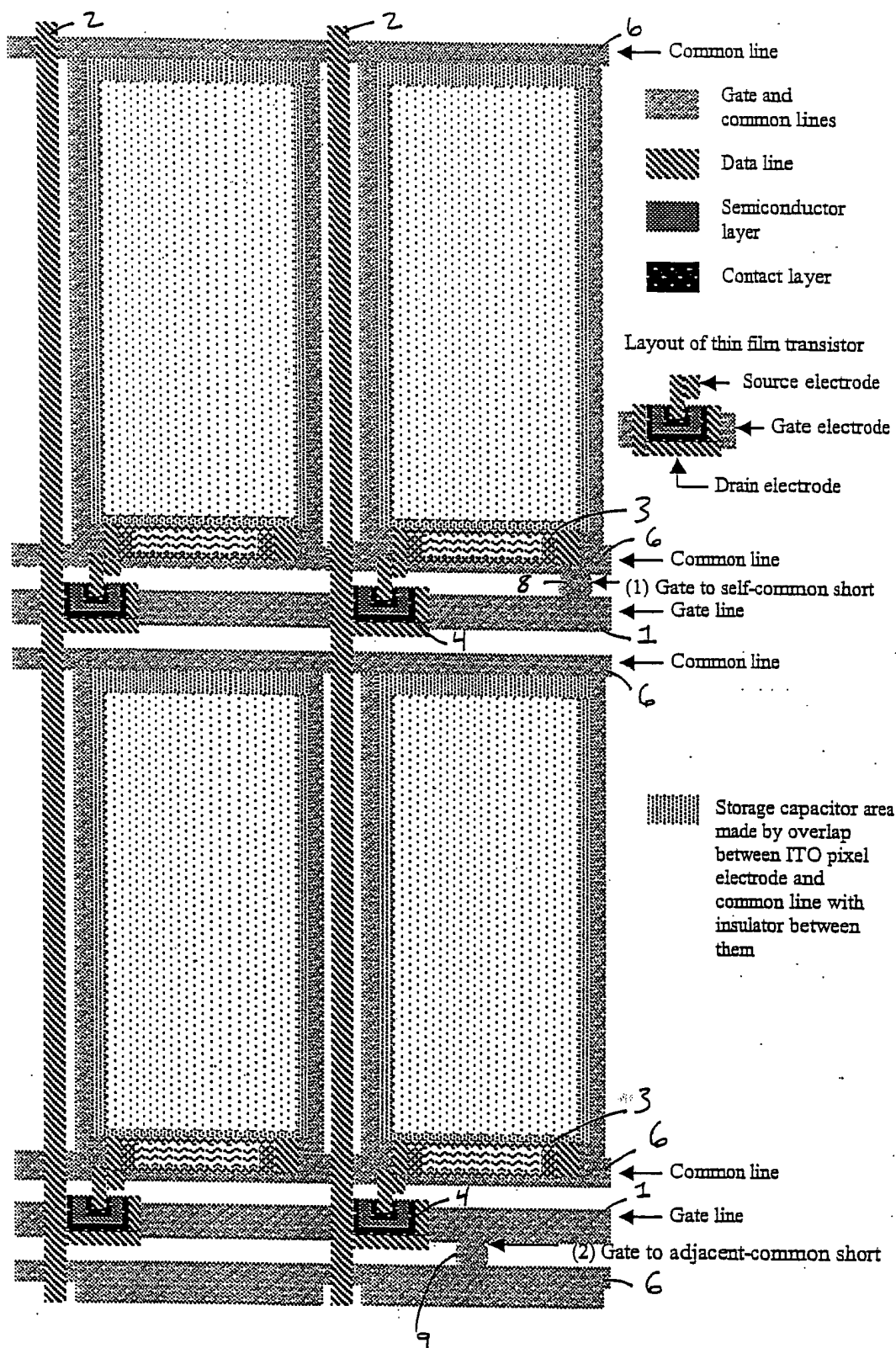


FIG. 1(b)

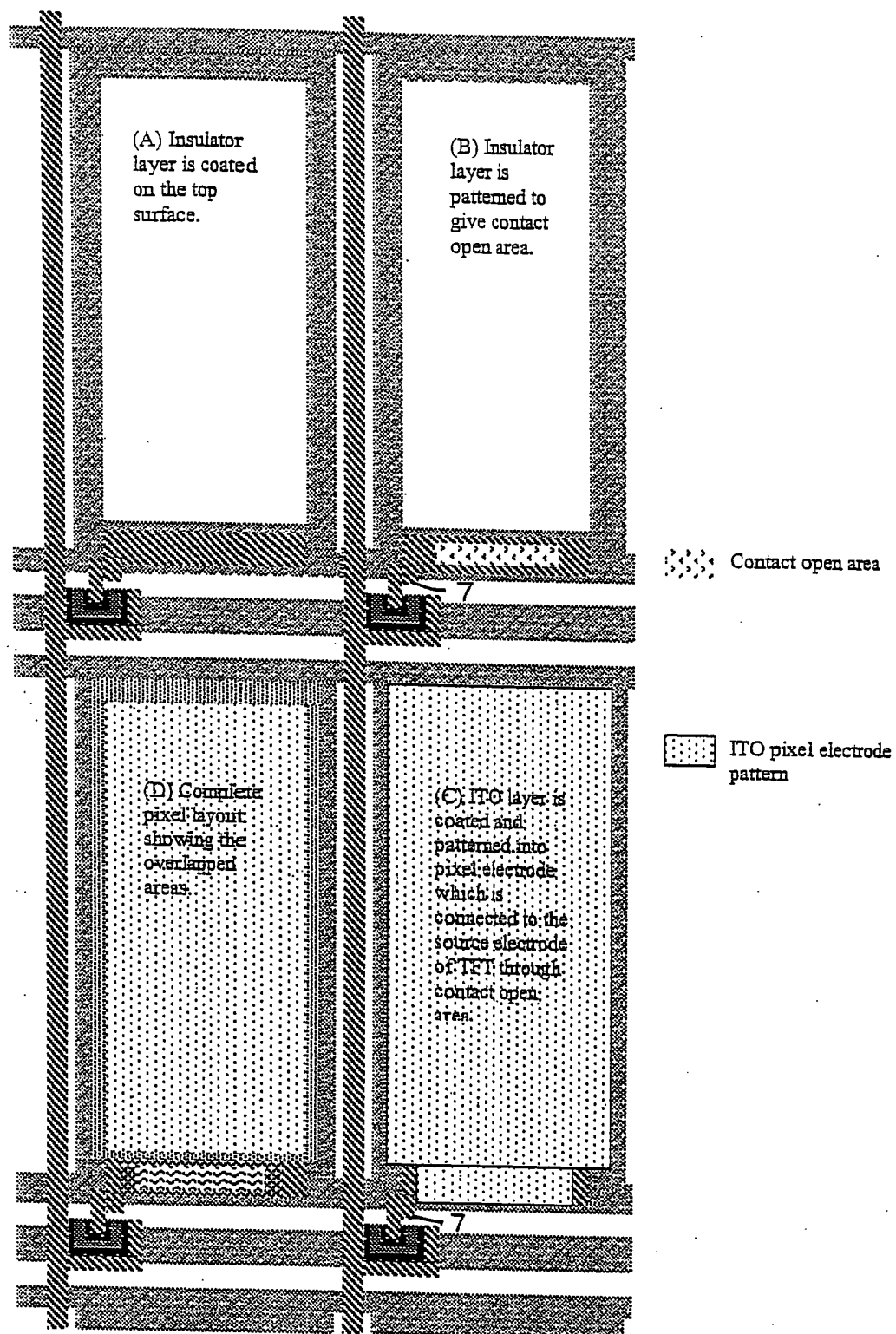


FIG. 2

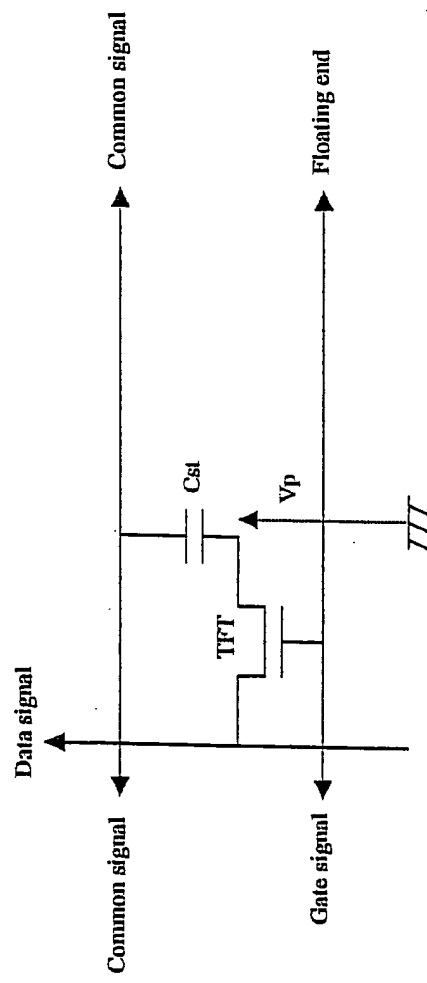


FIG. 3

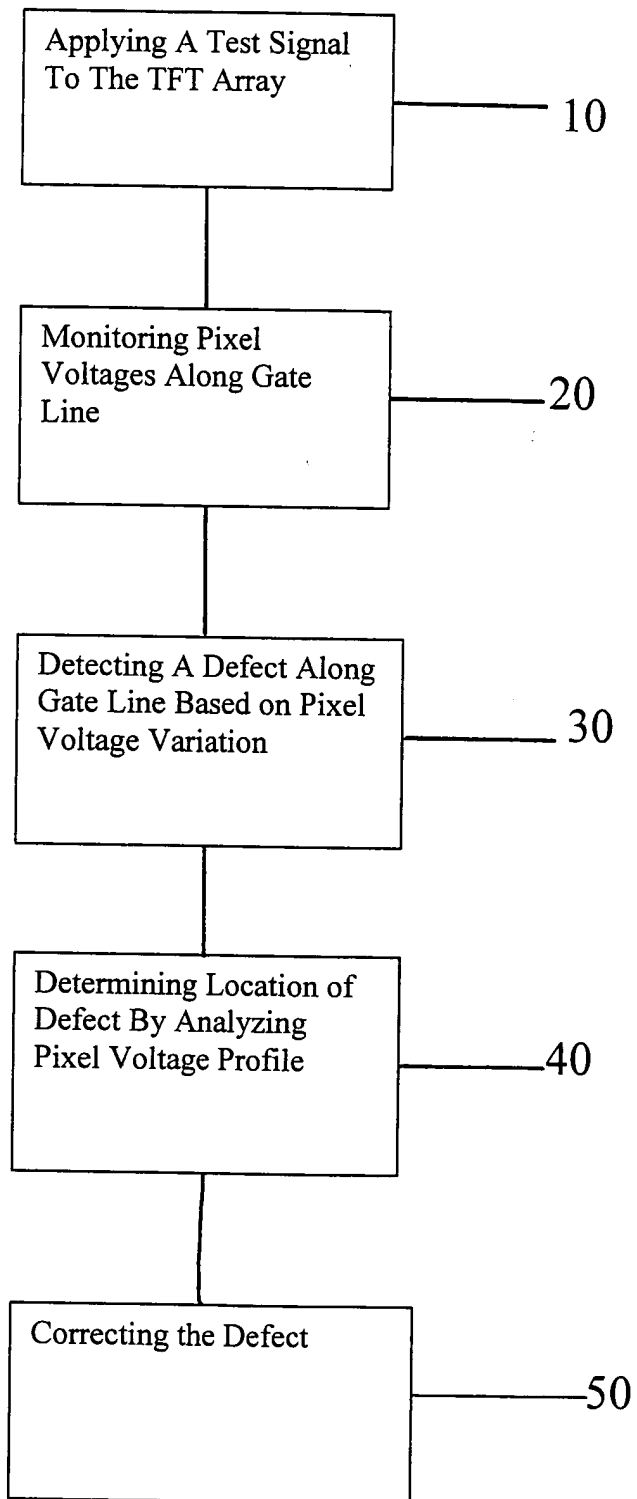
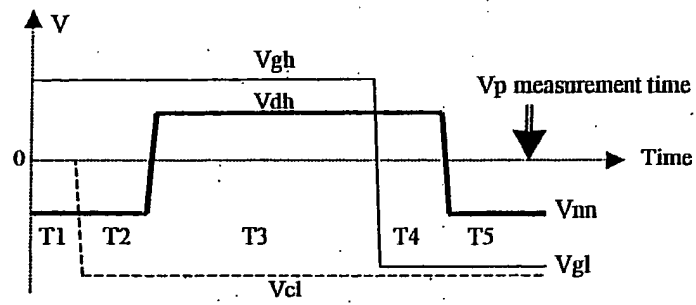


FIG. 4(a)



— Gate signal
 — Data signal
 - - - Common signal

FIG. 4(b)

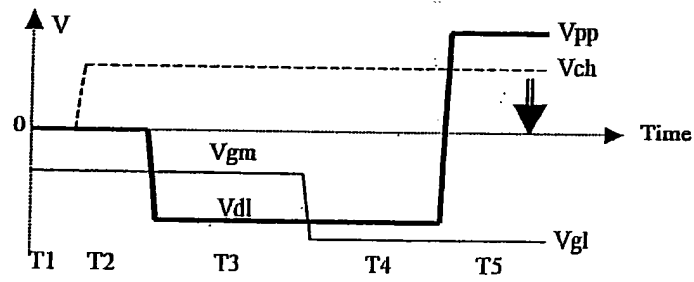


FIG. 5

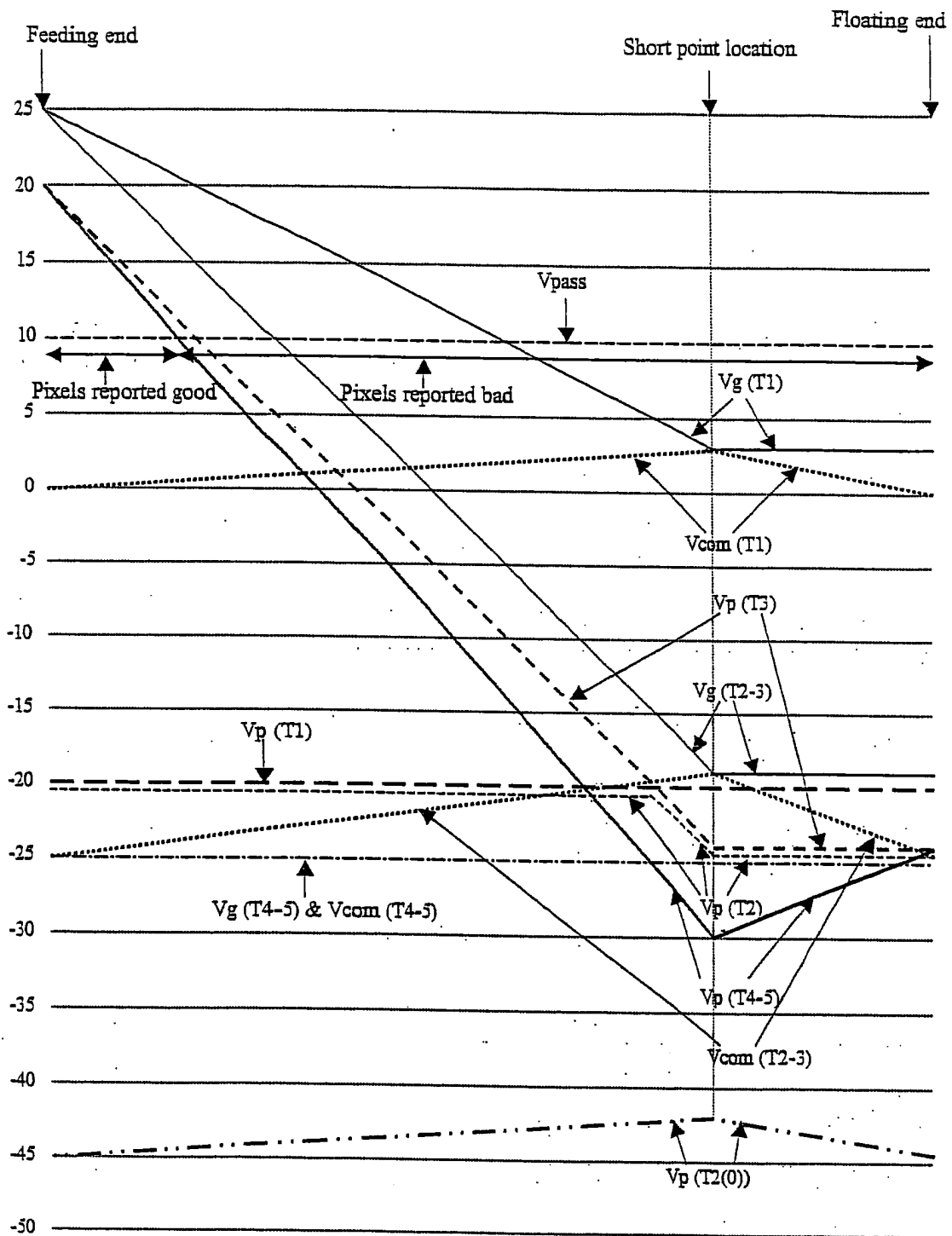


FIG. 6

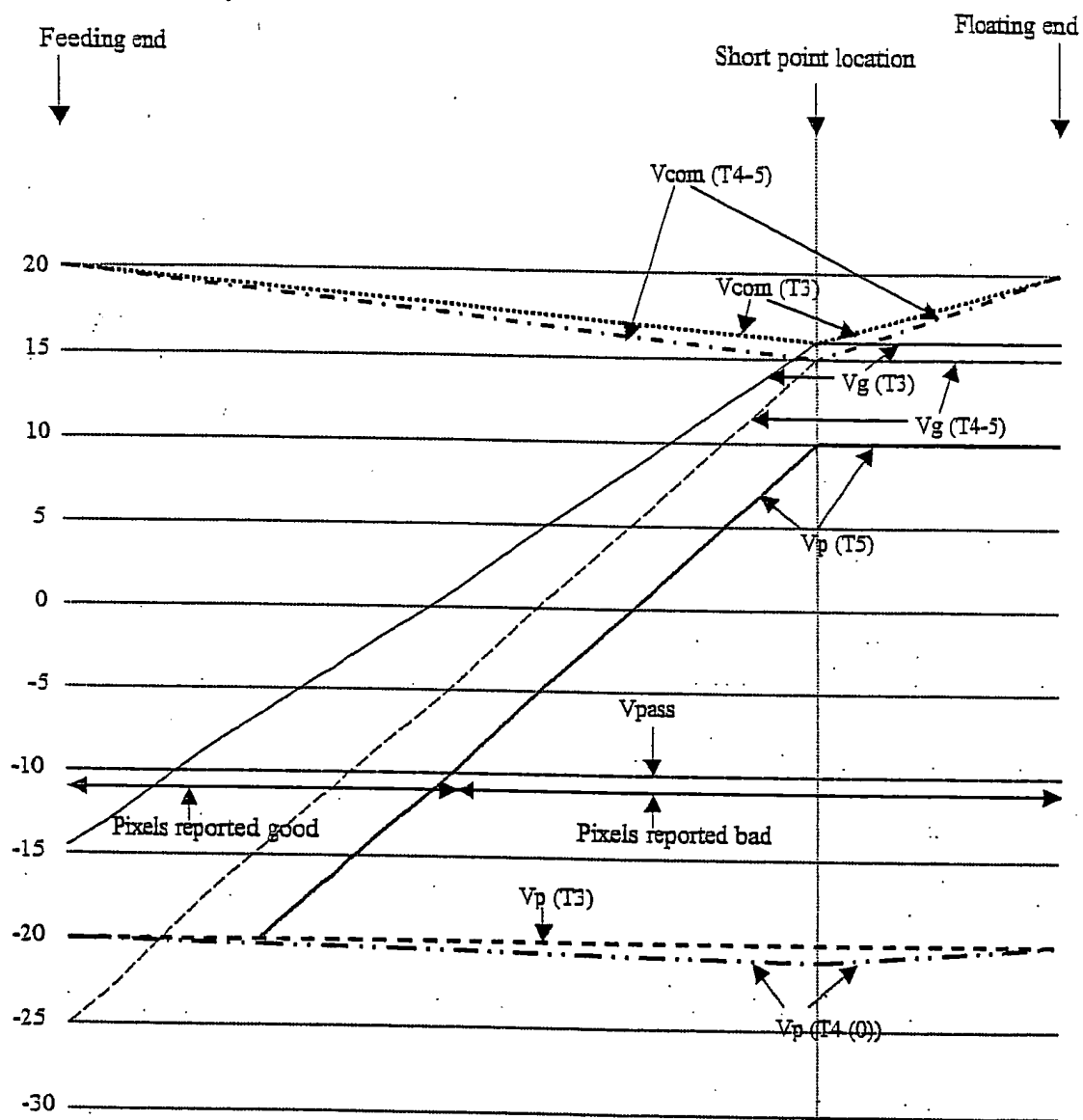


FIG. 7

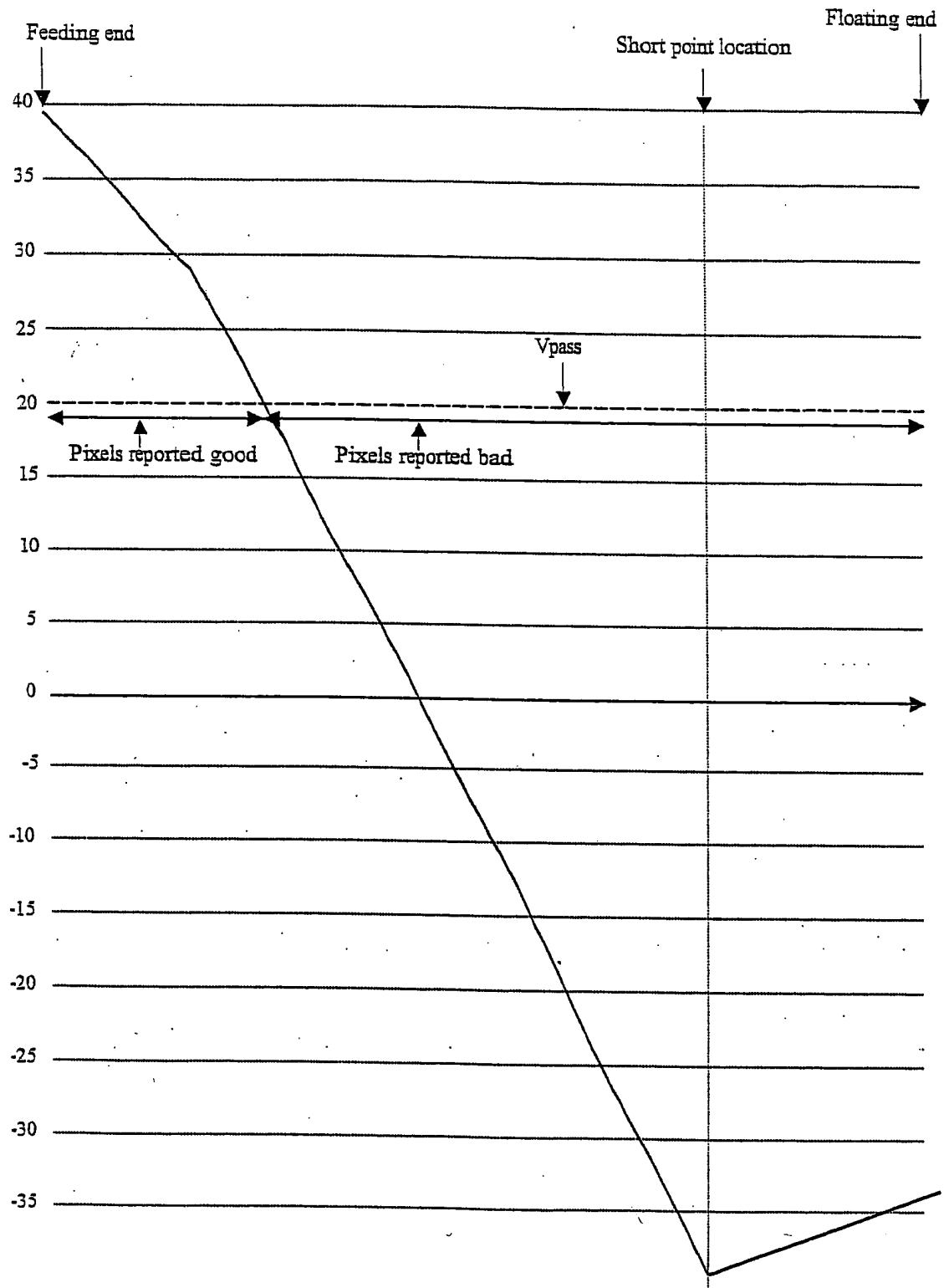


FIG. 8

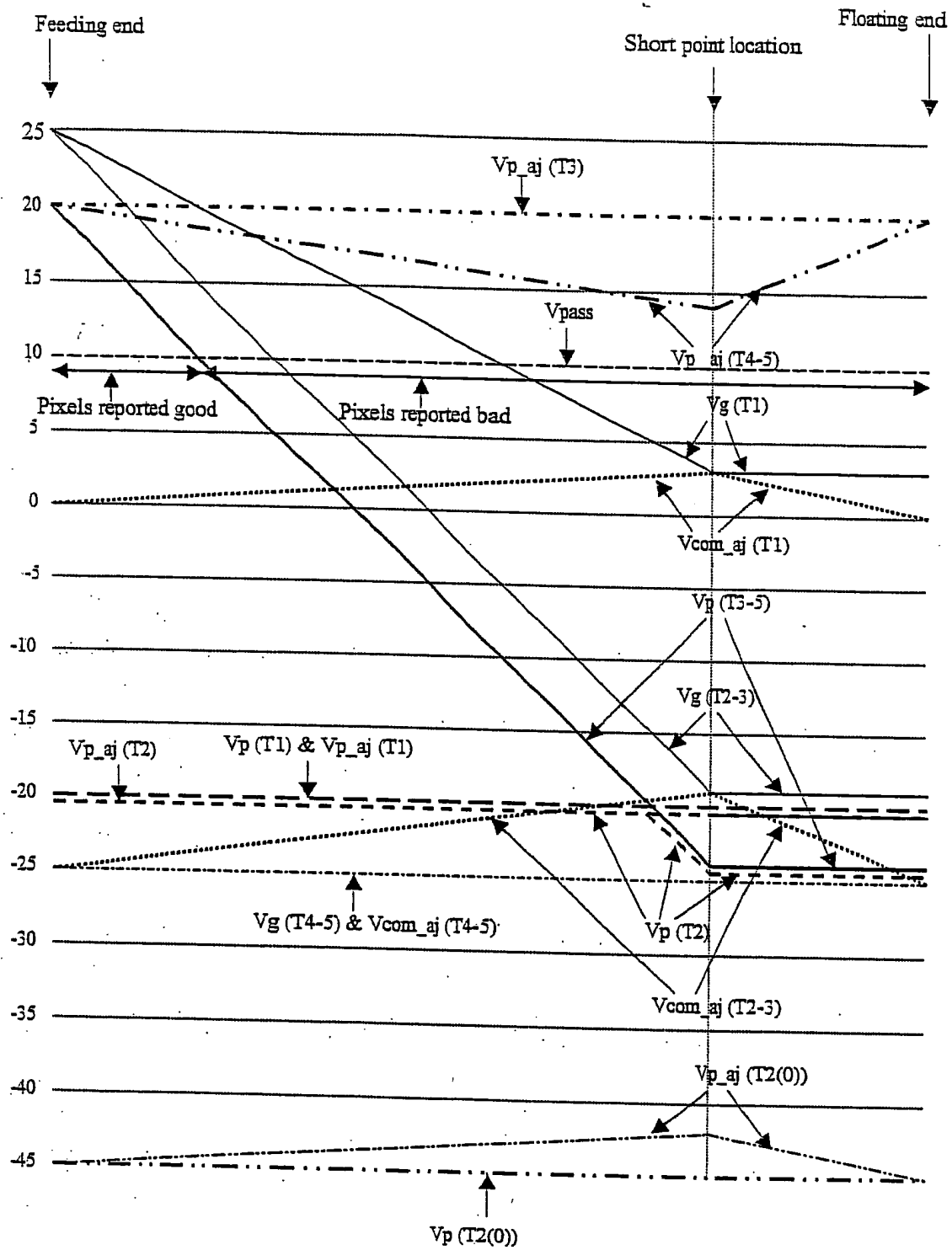


FIG. 9

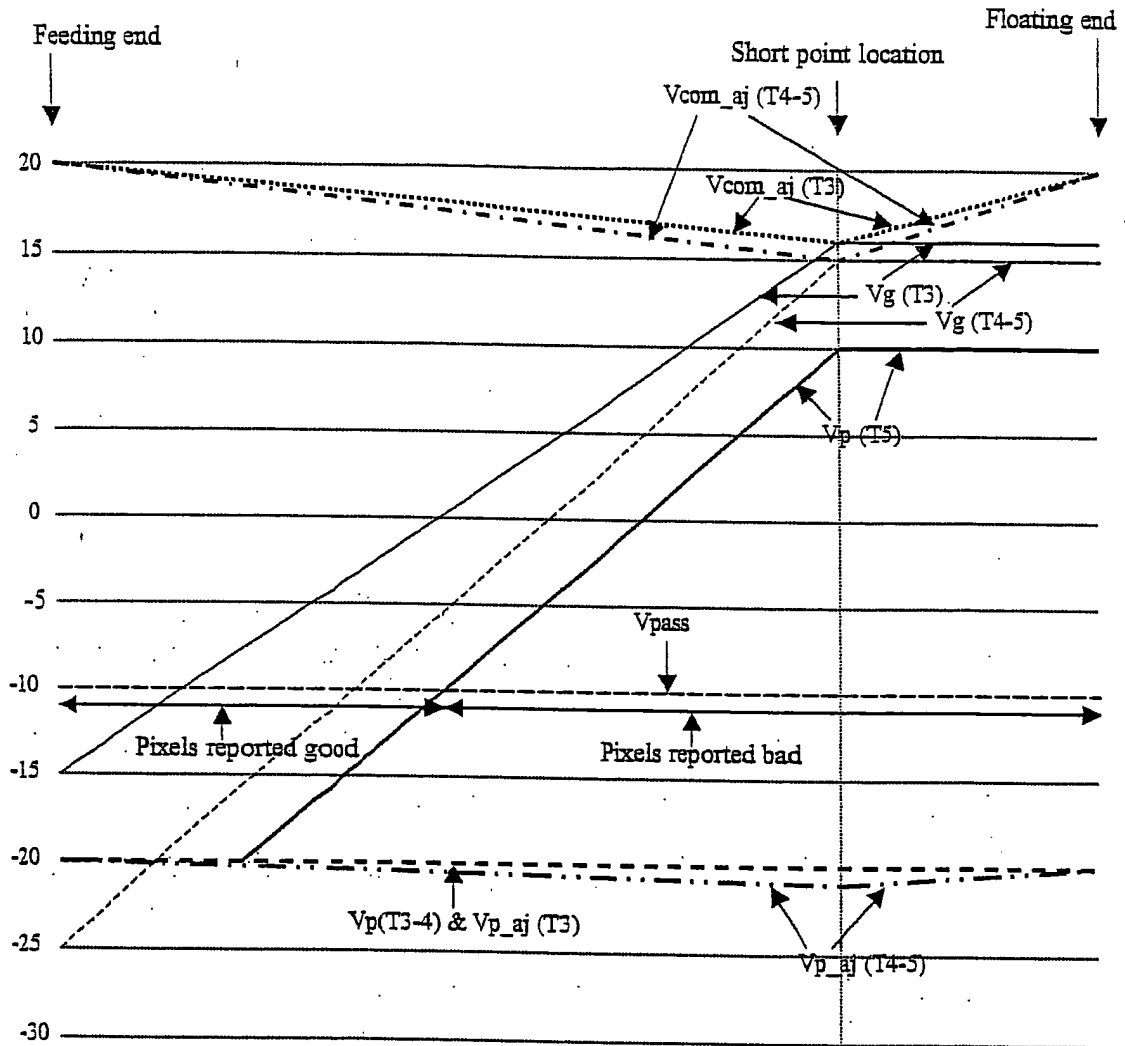


FIG. 10

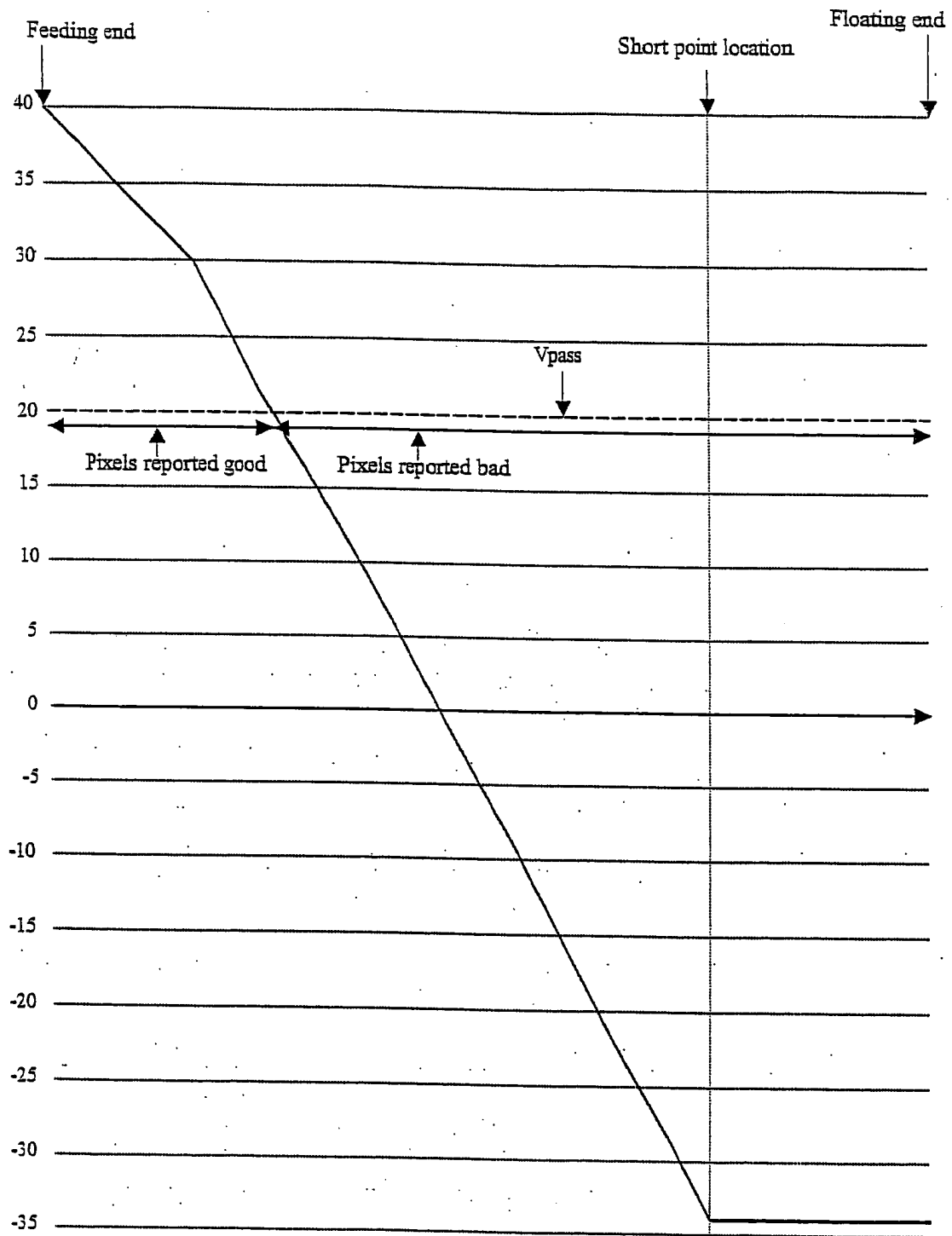


FIG. 11

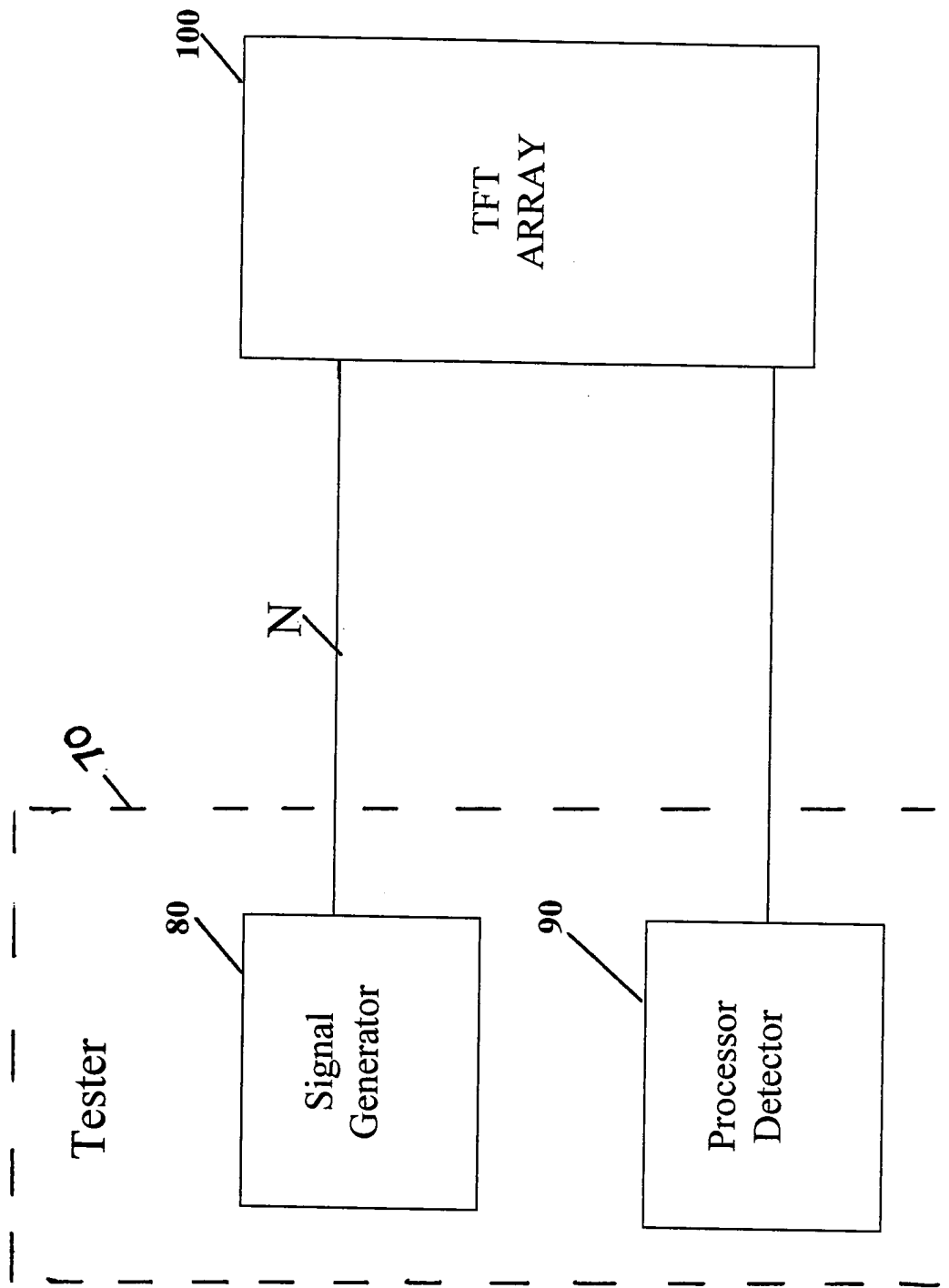


FIG. 12

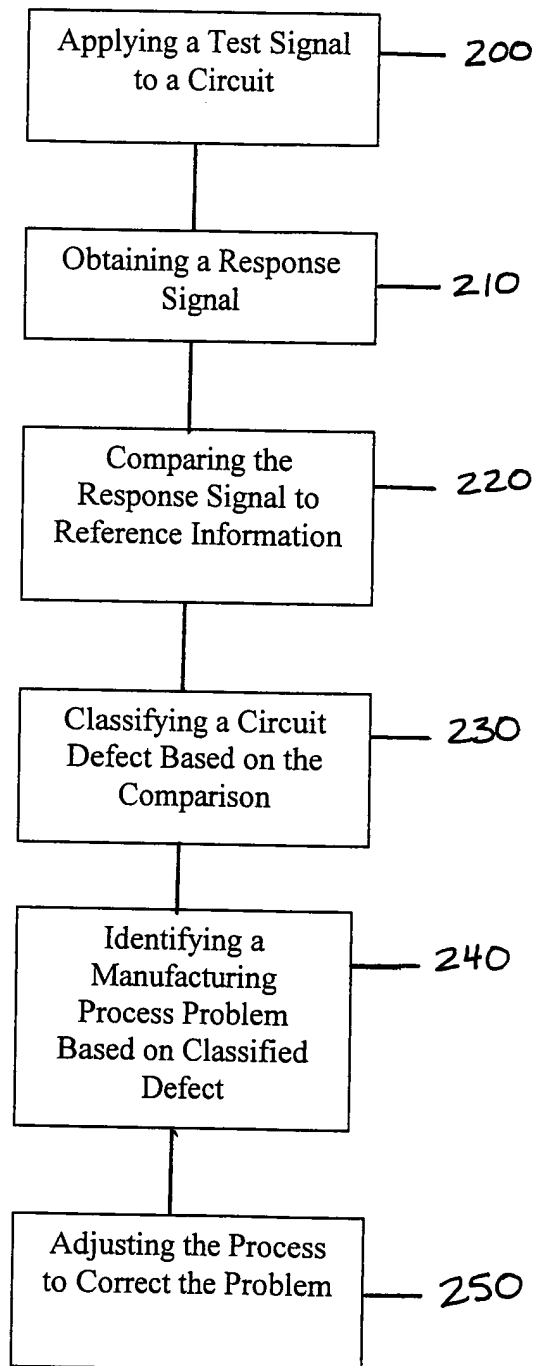


FIG. 13

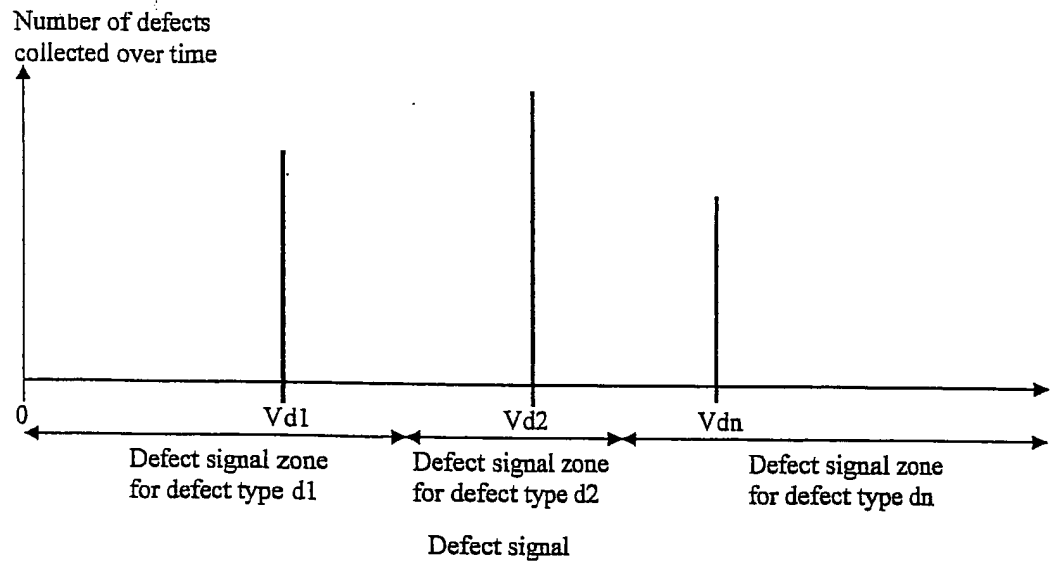


FIG. 14

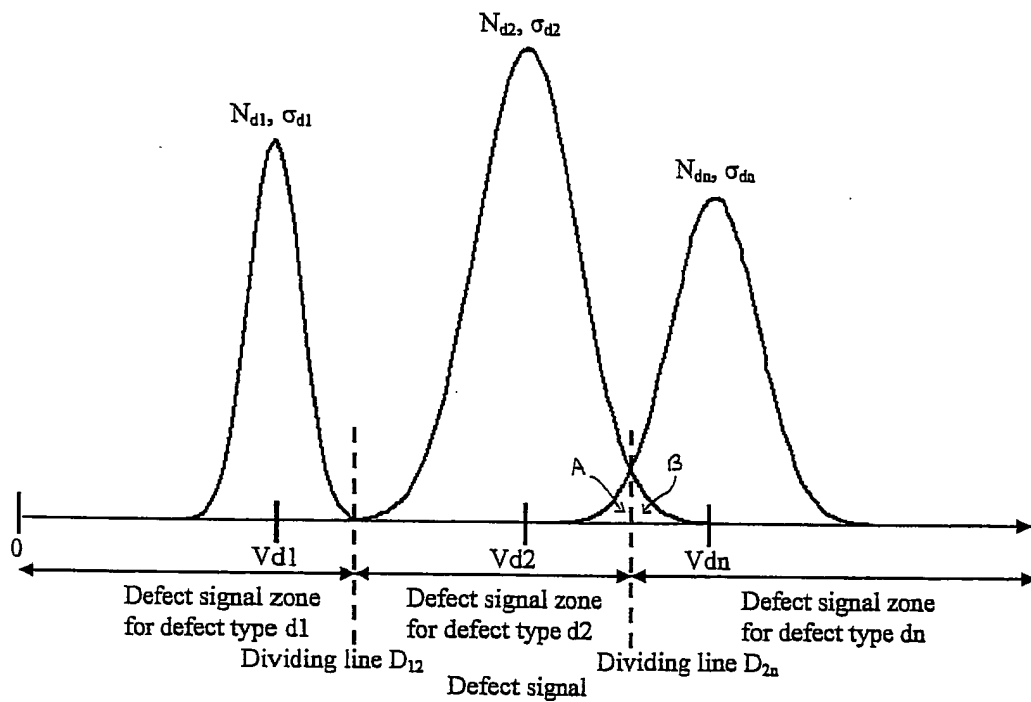


FIG. 15

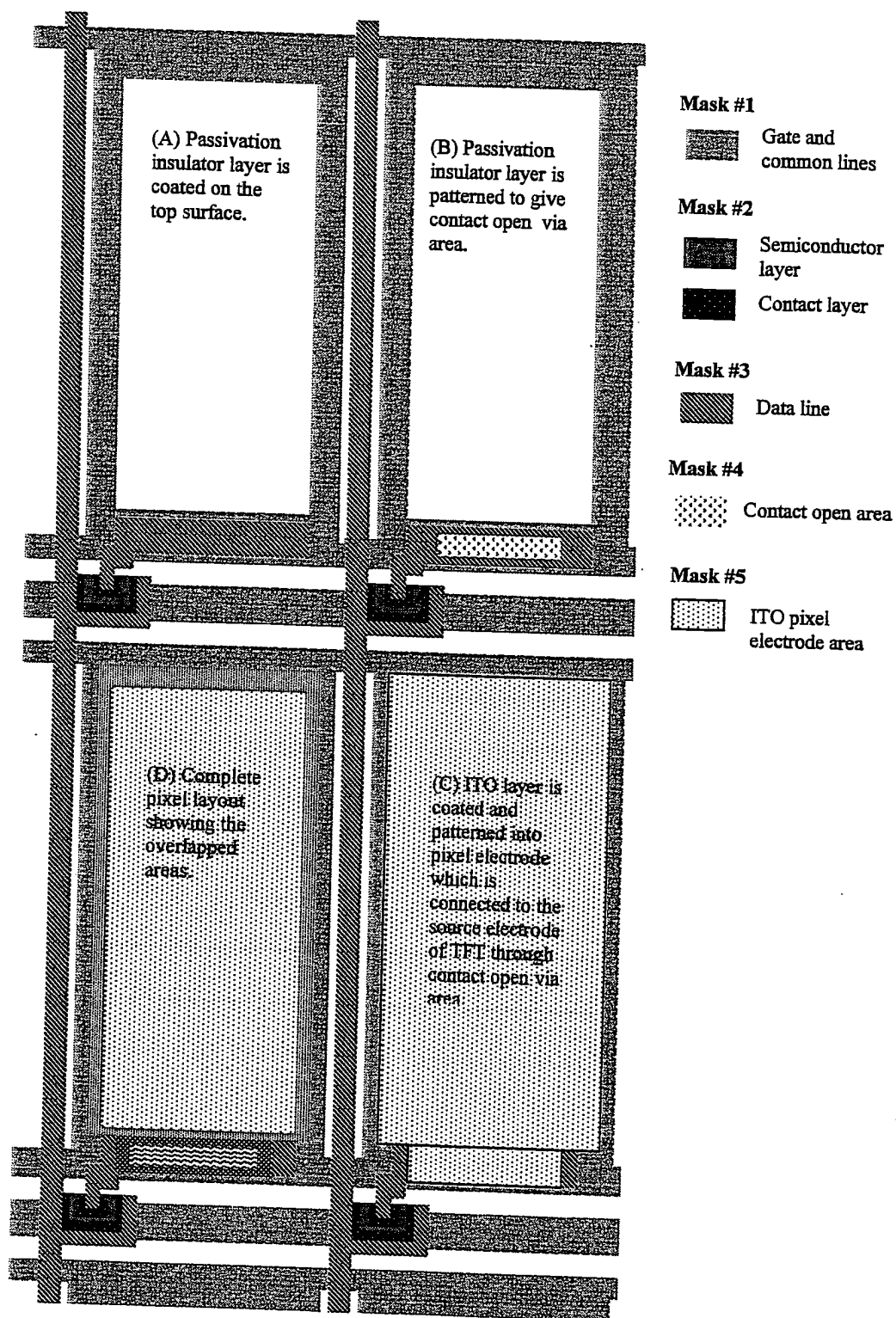


FIG. 16

